Indicate the mux signals supplied to the Forward A and Forward B muxes in this instruction execution sequence. For ease of grading. Just eliminate the ”00” mux signals.

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| Instruction | Forward A | Forward B |
| sub $s2, $s7, $s1 |  |  |
| sub $s5, $s1, $s3 |  |  |
| add $s1, $s4, $s2 |  | 01 |
| add $s7, $s3, $s0 |  |  |
| sub $s4, $s0, $s3 |  |  |
| sub $s4, $s2, $s5 |  |  |
| add $s4, $s7, $s2 |  |  |
| sub $s0, $s1, $s6 |  |  |
| add $s7, $s3, $s4 |  | 01 |
| sub $s1, $s4, $s3 |  |  |
| add $s4, $s1, $s6 | 10 |  |
| sub $s5, $s4, $s7 | 10 |  |
| add $s3, $s2, $s4 |  | 01 |
| add $s0, $s2, $s0 |  |  |
| add $s2, $s0, $s0 | 10 | 10 |
| sub $s6, $s5, $s2 |  | 10 |
| sub $s6, $s0, $s4 |  |  |
| add $s7, $s2, $s5 |  |  |
| sub $s4, $s1, $s1 |  |  |
| add $0, $s0, $s2 |  |  |
| add $s0, $s2, $s2 |  |  |
| sub $s5, $0, $s1 | 10 |  |
| sub $s3, $s3, $s4 |  |  |
| add $s3, $s5, $s3 | 01 | 10 |
| sub $s7, $s6, $s7 |  |  |
| sub $s7, $s3, $s0 | 01 |  |
| add $s4, $s4, $s4 |  |  |
| sub $s0, $s5, $s4 |  | 10 |
| add $s6, $s0, $s5 | 10 |  |
| sub $s2, $s3, $s2 |  |  |
| add $s6, $s7, $s7 |  |  |
| add $s5, $s5, $s3 |  |  |
| sub $s5, $s3, $s3 |  |  |
| sub $s1, $s5, $s0 | 10 |  |
| sub $s2, $s1, $s1 | 10 | 10 |
| add $s0, $s2, $s5 | 10 |  |
| sub $s7, $s3, $s2 |  | 01 |
| add $s0, $s7, $s3 | 10 |  |
| add $s4, $s6, $s7 |  |  |
| add $s3, $s5, $s3 |  |  |